



Indira Gandhi Delhi Technical University for Women

(Established by Govt. of Delhi vide Act 09 of 2012)

Kashmere Gate, Delhi - 110006

ICT based FDP on EMBEDDED SYSTEMS in collaboration with NITTTR Chandigarh'

Department of ECE organised a one-week [CT based FOP on EMBEDDED SYSTEMS in collaboration with NITTTR Chandigarh. The program was held during 19th March to 23rd March 2018. The venue was Embedded System Design Lab, Electrical block, IGDTUW. Course Content covered in OL1e week TCT included Introduction to Micro-controllers like PIC, AVR, Embedded processors, Reconfigurable devices like MMX series, MIPS, ARM System on Chip, Applications specific Integrated Circuits, Modelling of multi-processors systems, Applications of embedded system designing includes Wireless Sensor & their networking. This had been broadcast directly from NITTTR Chandigarh, where eminent academic speakers from UIET, Clnncli garh, BBSBEC, Punjab. Thapar Un i vers i ty , N I T , Jal and har, Punjab Un i vers i ty, Chandigarh, SGS Electronics , Mohali and CSIO, Chandigarh. delivered their lectures.

The program was exceptionally beneficial for the faculty members of ECE, CSE and IT departments. The FDP was attended by nearly 30 faculties and 25 PhD scholars, 30 graduate and post graduate students Our sincere thanks to **Dr. Kanika Sharma, Course Coordinator, NITTTR, Chandigarh**, for giving us the opportunity to be one of the nodal centers for this ICT based Program on Embedded Systems.

Salient Features:

1. Renowned experts from Industry and Academia conducted the sessions.
2. Latest topics pertaining to the art: a wert: addressed.
3. The certificates of participation were given by NITTTR.
4. There was no registration fee for the same.





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Coordinator Dr Richa Yadav

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