



**Indira Gandhi Delhi Technical University For Women**  
(Established by Govt. of Delhi vide Act 09 of 2012)  
(Formerly Indira Gandhi Institute of Technology)  
Kashmere Gate, Delhi-110006

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

**ADVERTISEMENT FOR EMPANELMENT OF VISITING FACULTY**

Applications are invited for Empanelment of Professors/ Associate Professors/ Assistant Professors as Visiting Faculty in the Department of Electronics and Communication Engineering to teach various undergraduate and post graduate courses of Electronics and Communication Engineering and allied/related subjects of Artificial Intelligence as follows *but not limited to*:-

<b>ECE Subjects</b>	<b>ECE AI Subjects</b>
Information theory and coding	Digital Communication System
Microprocessor and microcontroller	Microprocessor and microcontroller
Embedded system	Optimization techniques and decision making
Digital Signal Processing	Object Oriented Programming
Information Retrieval	Semantic Web
Basic Electrical Engineering	Recent trends in AI
Basics of Electrical & Electronics Engineering	AI for games
Signals and Systems	AI in healthcare
Network Analysis and Synthesis	Linear Integrated Circuits

For other subjects please refer to Scheme and Syllabus of courses in Department of ECE

[https://drive.google.com/drive/folders/1baY8InLFPCeDsimhiKy\\_KMR5NQBH07XR?usp=drive\\_link](https://drive.google.com/drive/folders/1baY8InLFPCeDsimhiKy_KMR5NQBH07XR?usp=drive_link)

For Remuneration Details, Guidelines and Eligibility Conditions please refer to Annexure 1

1. Application should be submitted online through Google form at the link:-  
<https://forms.gle/w8FG9LB4LtPrmth8>
2. **The last date for submission of application is 23/12/2024, 11:59 PM.**
3. The shortlisted candidates will be called for Interview/Interaction with the Expert Committee on 26-27 December, 2024 on-Campus.
4. All classes (Theory/Lab) to be conducted in-person on-campus at Kashmere Gate, Delhi.

(HOD, ECE)

## ANNEXURE-1

### Guidelines and Eligibility Conditions

A) Applications are invited for Empanelment of Part-Time/Guest/Visiting Faculty in the Department of Electronics and Communication Engineering to teach various subjects in B. Tech (ECE), B.Tech (ECE AI) programs.

(1) The rate for remuneration to the Visiting Faculty as per the University norms are as under:

S. No	Faculty	Proposed Rates (per hour)		Max. Ceiling per month in Rupee
		Theory	Lab	
1	Professor	Rs. 2750/-	1375/-	Rs. 70,000/-
2	Associate Professor & Assistant Professor who has completed 3-Years of service in the AGP 8000 or Equivalent	Rs. 2250/-	1125/-	Rs. 70,000/-
3	Assistant Professor (not covered above)	Rs. 1750/-	875/-	Rs. 52000/-

(2) Engagement of the Visiting Faculty shall be made on hourly basis from the approved panel.

(3) The faculty so engaged shall have no claim what so ever for Continuation/ Extension/ Regularization of his/her services in the University.

(4) In case of any discrepancies, the decision of Competent Authority will be final.

### B) Eligibility Conditions: -

**Name of the Post: Professor (Engineering)**

#### Essential Qualification:

- (i) Ph.D in the relevant field of Engineering and Technology
- (ii) M.E./M. Tech and B.E./B. Tech. or equivalent in relevant discipline with first class either at Bachelor or Master Level.

#### Experience:

- (i) Minimum of 10 years teaching research/industrial experience (excluding the period spent on obtaining the Research Degree on Full time basis), of which at least 03 years should be at the level of Associate Professor in relevant discipline. 03 years Post Ph.D. experience is essential.
- (ii) The teaching experience should be in a minimum degree level institution.

#### Desirable:

- (i) Teaching, research industrial and/or professional experience in a reputed organisation.
- (ii) Published work, such as research papers, patents filed/obtained, books and/or technical reports.

- (iii) Experience of guiding the project work/dissertation of PG/Research Students or supervising R & D Projects in industry.
- (iv) Demonstrated leadership in planning and organising academic, research, industrial and/or professional activities; and
- (v) Capacity to undertake/lead sponsored R&D, consultancy and related activities

**Name of the Post: Associate Professor (Engineering)**

**Essential Qualification:**

- (i) Ph.D in the relevant field of Engineering and Technology
- (ii) M.E./M. Tech and B.E./B. Tech. or equivalent in relevant discipline with first class either at Bachelor or Master Level.

**Experience:**

- (i) Minimum of 08 years teaching /research/industrial experience (excluding the period spent on obtaining the Research Degree on Full time basis), at the level of Assistant Professor in relevant discipline.
- (ii) The teaching experience should be in a minimum degree level institution

**Desirable:**

- (i) Teaching, research industrial and/or professional experience in a reputed organisation.
- (ii) Published work, such as research papers, patents filed/obtained, books and/or technical reports.
- (iii) Experience of guiding the project work/dissertation of PG/Research Students or supervising R & D Projects in industry.
- (iv) Demonstrated leadership in planning and organising academic, research, industrial and/or professional activities; and
- (v) Capacity to undertake/lead sponsored R&D, consultancy and related activities

**Name of the Post: Assistant Professor (Engineering)**

**Essential Qualification:**

M.E./M. Tech and B.E./B. Tech. or equivalent in relevant discipline with first class either at Bachelor or Master Level.

**Desirable:**

- (i) Ph.D in relevant discipline and/or published research papers in journals of high repute.
- (ii) Experience in teaching at Degree Level institution in relevant discipline.